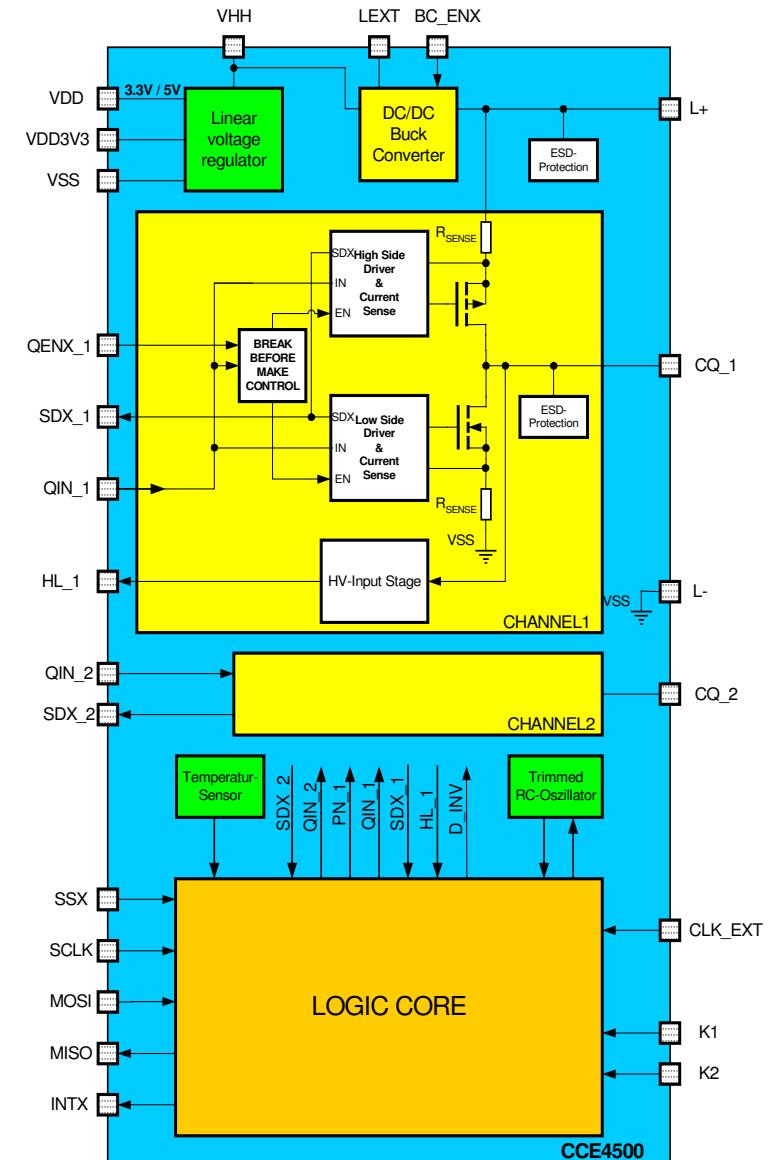


IO-Link System IC

Features:

- IO-Link compliant High-Voltage Input and Output driver
- 2nd switch output with IO-Link compliant driver features
- IO-Link Transfer rates: 4.8 kBit, 38.4 kBit, 230.4 kBit
- DC/DC Buck-converter for dissipation minimization
- Programmable linear 5/3.3V Low-Drop Voltage regulator
- Temperature compensated OnChip-RC-Oscillator
- OnChip-Over-Temperature detection
- Logic functions can be switched off
- Integrated UART and Protocol-handler
- Serial (SPI) µC-Interface
- Multiple configuration options using registers and NVM
- QFN24-package or SSOP24

Parameters	Cond.	Min.	Typ.	Max.	Unit
Supply voltage	$V_{L+} - V_{L-}$	9	24	30	V
IO-Link Input voltage	$V_I = V_{C/Q} - V_{L-}$	-1		34	V
IO-Link Output voltage				400	mA
Output voltage DC/DC-Converter		6	7	8	V
Efficiency DC/DC-Converter		80			%
Switching frequency DC/DC-Converter		0.8		1.4	MHz
Output current DC/DC-Converter				100	mA
Output voltage Low-Drop-Regulator	PROGBIT= 3.3V	4,75	5	5,25	V
	PROGBIT= 5 V	3,1	3,3	3,5	V
Output current Low-Drop-Regulator				60	mA
Power dissipation	QFN24-package			1	W
Temperature range	θ_{op}	-40		85	°C
Over-temperature switching off			130		°C
Frequency variation	OnChip-Oscillator			2	%



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